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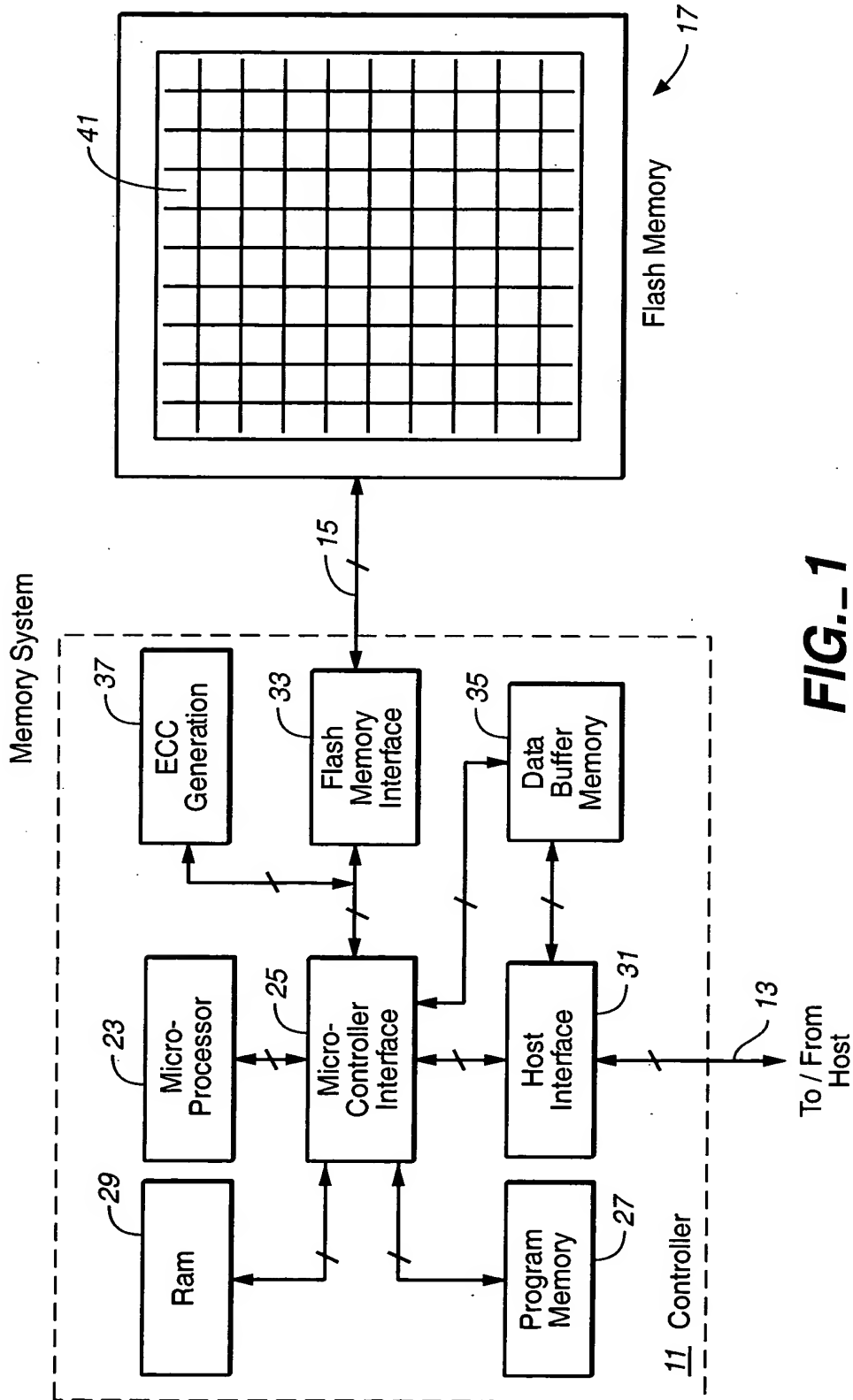


FIG. 1

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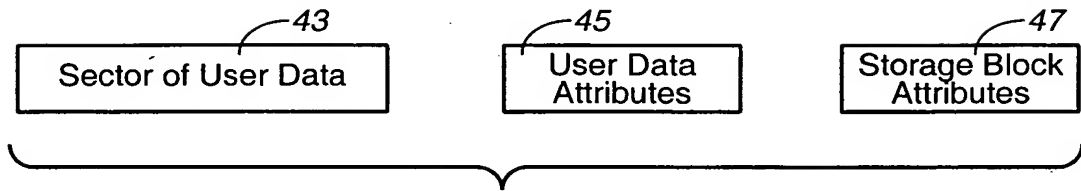


FIG. 2
(PRIOR ART)

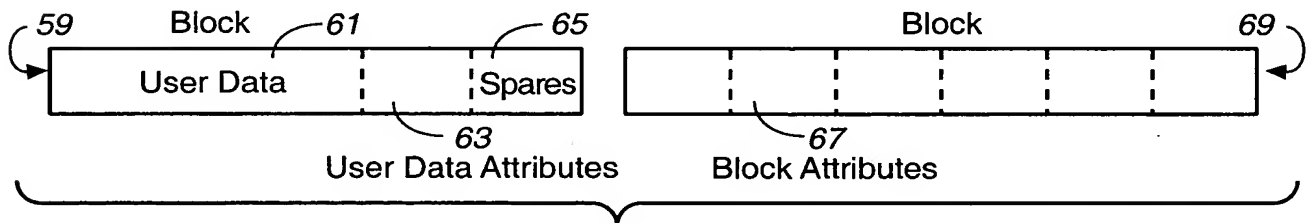
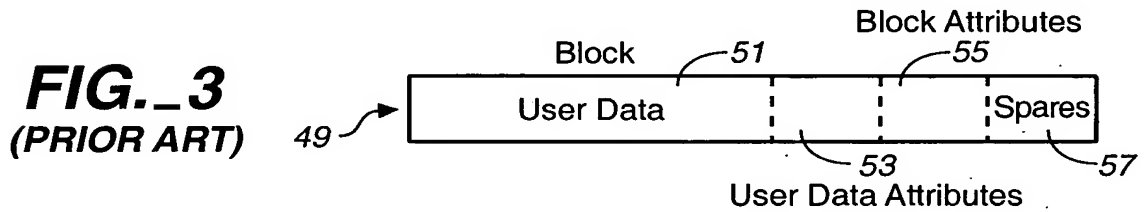


FIG. 4
(PRIOR ART)

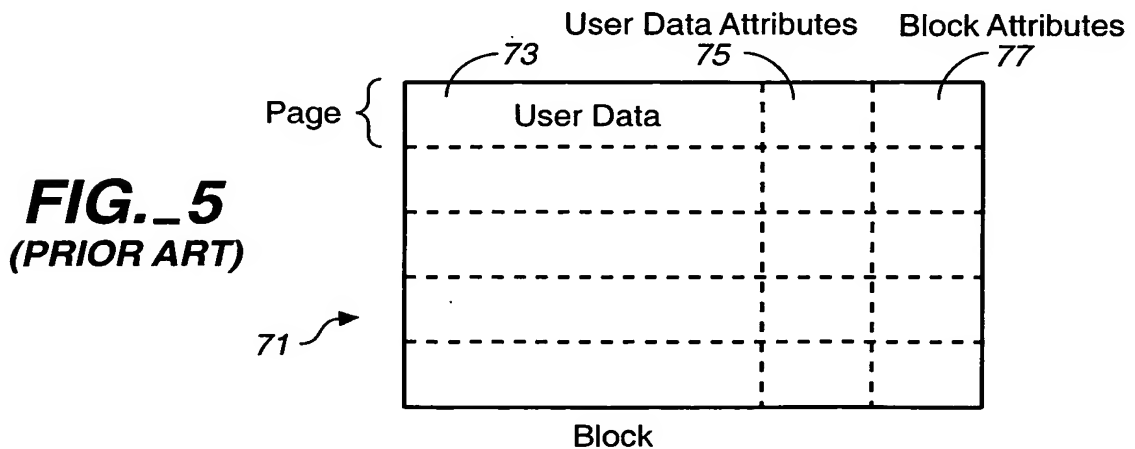


FIG. 5
(PRIOR ART)

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FIG._6A

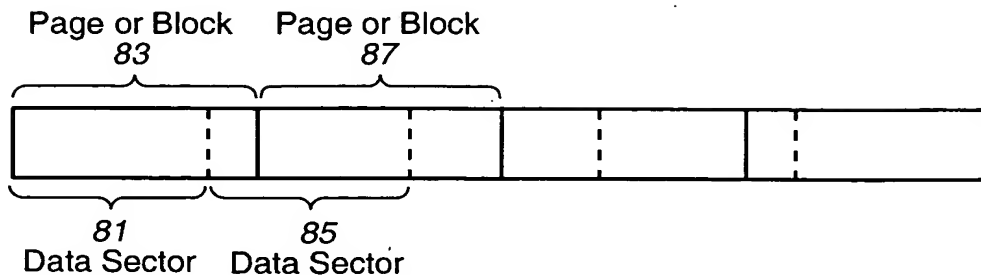


FIG._6B

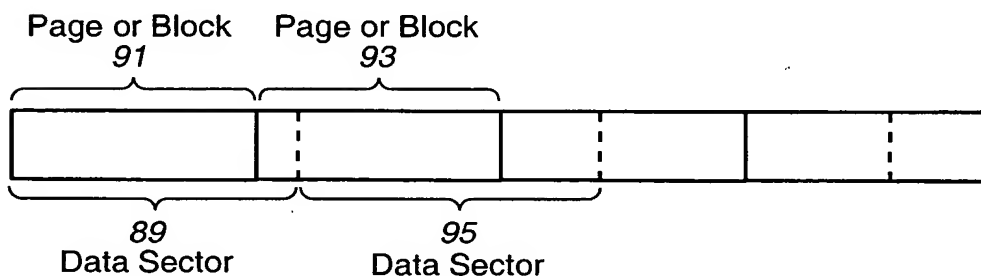


FIG._8

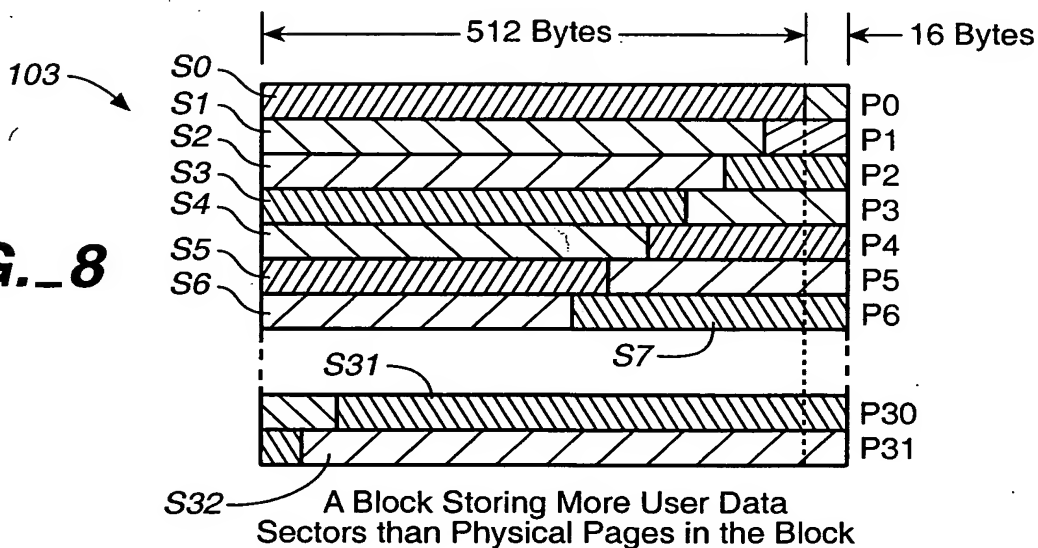
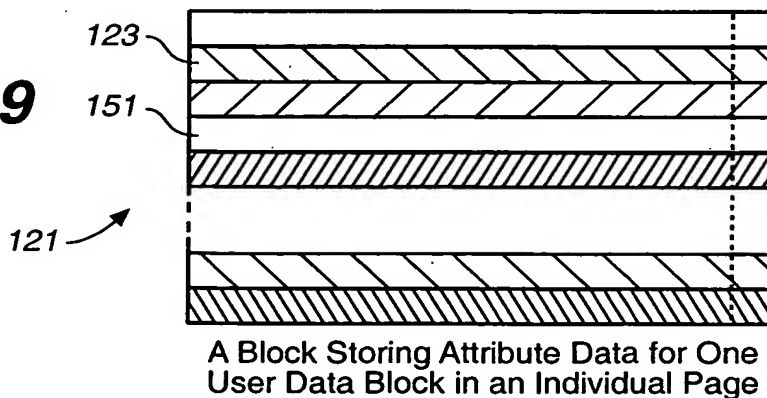
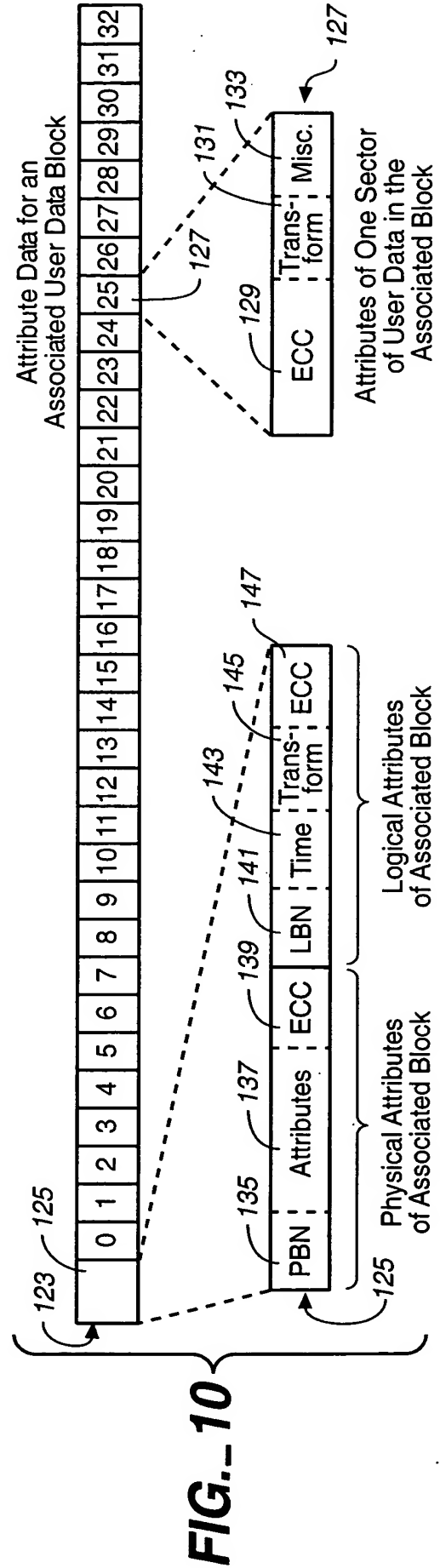
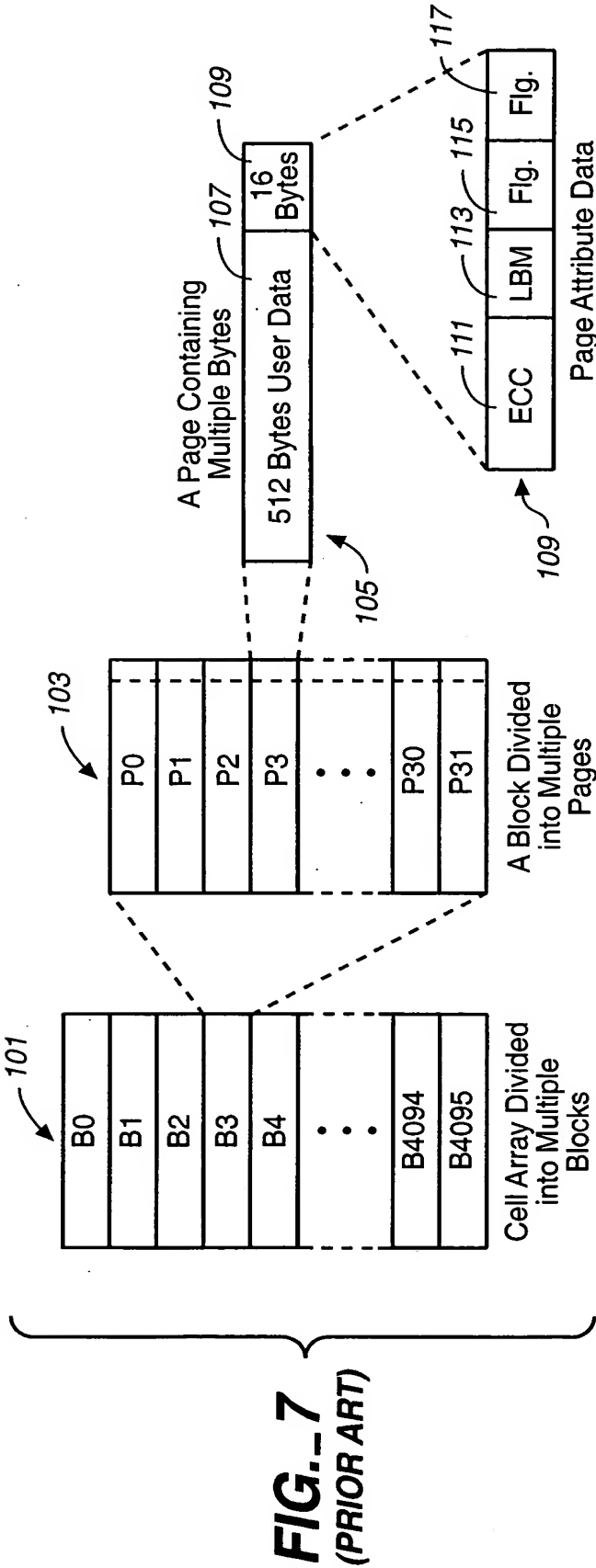


FIG._9



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Table in Controller RAM Built From
 Fig. 10 Attribute Records

Address (LBN)	User Data Block	Attribute Data Page	
	PBN	PBN	Page No.
25			
26			
27			
28			

155 → 153 → 157 → 159 →

FIG._11

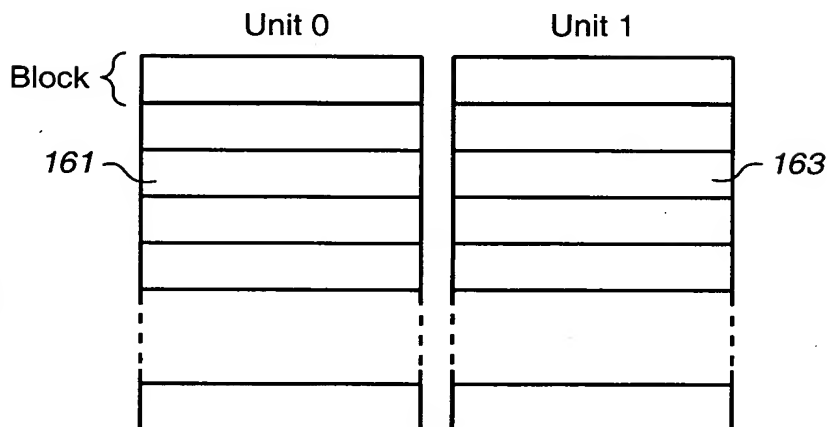


FIG._12
 (PRIOR ART)

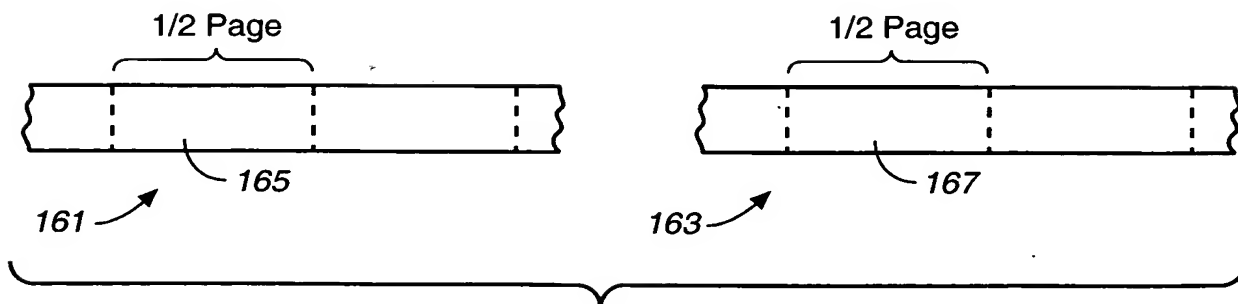


FIG._13